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Matching Network Elimination in Broadband Rectennas for High-Efficiency Wireless Power Transfer and Energy Harvesting

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Abstract— Impedance matching networks for nonlinear devices such as amplifiers and rectifiers are normally very challenging to design, particularly for broadband and multiband devices. A novel design concept for a broadband high efficiency rectenna without using matching networks is presented in this paper for the first time. An off-center-fed dipole antenna with relatively high input impedance over a wide frequency band is proposed. The antenna impedance can be tuned to the desired value and directly provides a complex conjugate match to the impedance of a rectifier. The received RF power by the antenna can be delivered to the rectifier efficiently without using impedance matching networks, thus the proposed rectenna is of a simple structure, low cost and compact size. In addition, the rectenna can work well in different operating conditions and using different types of rectifying diodes. A rectenna has been designed and made based on this concept. The measured results show that the rectenna is of high power conversion efficiency (over 60%) in two wide bands, which are 0.9–1.1 GHz and 1.8–2.5 GHz respectively, for mobile, Wi-Fi and ISM bands. Moreover, by using different diodes, the rectenna can maintain its wide bandwidth and high efficiency over a wide range of input power levels (from 0 to 23 dBm) and load values (from 200 to 2000 Ω). It is therefore suitable for high efficiency wireless power transfer or energy harvesting applications. The proposed rectenna is general and simple in structure without the need for a matching network hence is of great significance for many applications.

Index Terms— Broadband rectennas, impedance matching networks, off-center-fed dipole, wireless power transmission, wireless energy harvesting;

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I. INTRODUCTION

IMPEDANCE matching is a basic but crucial concept in electronics and electrical engineering, since it can maximize the power transfer from a source to a load or minimize the signal reflection from a load. In the wireless industry today, there have been many devices (such as oscillators, inverters, amplifiers, rectifiers, power dividers, boost converters) and systems that have a high demand for impedance matching networks. A number of techniques for the network design have been reported [1]–[6]. Among them, rectifiers and power amplifiers normally utilize nonlinear elements such as diodes and transistors in the circuits. Hence their input impedance varies with the frequency, input power and load impedance. The impedance matching networks for such nonlinear circuits become very challenging to design.

Wireless power transfer (WPT) and wireless energy harvesting (WEH) have attracted significant attention in the past few years [7]–[10]. In both radiative and inductive wireless power transmissions, the rectifiers are a vital device for converting AC or RF power to DC power, while impedance matching networks are required to achieve high conversion efficiency [9].

A rectifying antenna (*rectenna*) is one of the most popular devices for WPT and WEH applications, and much progress has been made [11]–[19]. Multiband and broadband rectennas [15]–[19] can receive or harvest RF power from different sources and from different channels simultaneously, thus they outperform the conventional single band rectennas [11]–[14] in terms of overall conversion efficiency as well as total output power. However, the design of the impedance matching network for broadband or multiband rectennas is very challenging, and the structure of the matching network is relatively complex which may increase the cost and loss, and also introduce errors in manufacture.

Some techniques such as resistance compression networks and frequency selective networks, have been developed to reduce the non-linear effects of the rectenna [20]–[24] so that the performance can be maintained in different operating conditions. But, they all require introduction of further circuit components in the matching network which increases the

complexity of the overall design. Using more components could increase the loss and decrease the overall efficiency. A need exists therefore for rectennas comprising simple structures with competitive performance. It is desirable that the impedance matching network is eliminated or simplified, but the received RF power at different frequency bands can still be delivered to the rectifier with high RF-DC conversion efficiency.

Some designs use a standard antenna with $50\ \Omega$ impedance to match with a rectifier. Thus either the operating bandwidth is narrow [25], or the conversion efficiency over the broadband is low, typically $< 20\%$ [26]. So far there are no available designs without matching networks, that can produce high conversion efficiency over a wide frequency band, and there are no available approaches that can tune the antenna impedance to the desired value to match with the impedance of the rectifier.

In this paper, we propose a novel methodology for a high efficiency broadband rectenna without the use of a matching network. The concept and operating mechanism are introduced in Section II. The approaches for designing a broadband high impedance antenna are discussed in Section III. The rectenna integration that can eliminate the use of matching networks is shown in Section IV. The experimental validations and measurements of a fabricated rectenna example are shown in Section V. To the best of our knowledge, the proposed design is the first broadband rectenna without using matching networks and achieves good performance; that is, high RF-DC conversion efficiency and improved linearity over a wide frequency band, a range of input power levels and load impedance.

II. NOVELTY OF THIS WORK

A conventional rectifying antenna system, as shown in Fig. 1, normally consists of five different parts:

- 1) A receiving antenna which is configured to receive RF signals from a predetermined source (WPT) or to receive random signals in the ambient environment (WEH). The input impedance of the antenna is usually matched to standard $50\ \Omega$;
- 2) A band pass filter to reject the higher order harmonic signals generated by the rectifier, since the signals could be radiated by the antenna which might reduce the overall conversion efficiency and cause interference. The filter can either be embedded with the antenna to produce a filtering-antenna structure [27] or be integrated with the impedance matching network [18] to make the complete design simple and compact;
- 3) An impedance matching network which is configured to match the complex impedance of the rectifier to a resistive port (e.g. $50\ \Omega$). Thus the power of the received signals could be fully delivered to the rectifier;
- 4) A rectifier which is configured to convert RF power to DC power. The input impedance of the rectifier varies in a wide range of values and the impedance is very sensitive to the variation of frequency, input power and load impedance;
- 5) A load which could typically be a resistor, a DC-to-DC boost converter for realizing a higher output voltage, or a super capacitor to store energy.

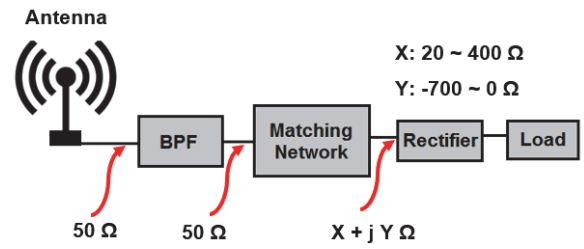


Fig. 1. Configuration of a conventional rectifying antenna system with impedance matching networks.

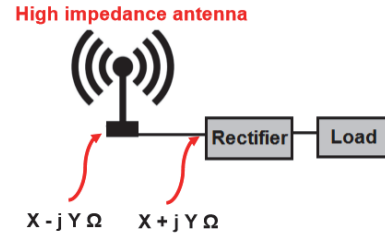


Fig. 2. Configuration of the proposed rectifying antenna without using impedance matching networks.

In previous studies [18], [24], the impedance of the rectifier was analysed under different operating conditions such as a wide frequency range (e.g., $0.5 - 3\ \text{GHz}$), a range of input powers (e.g., -40 to $0\ \text{dBm}$) and a wide load impedance range (e.g., 1 to $100\ \text{k}\Omega$). It is concluded that the input impedance of the rectifier varies significantly (20 to $400\ \Omega$ for the real part, 0 to $-700\ \Omega$ for the imaginary part) over these operating conditions. Furthermore, due to nonlinearity, the impedance of the rectifier would also vary with different types of rectifying diodes and different circuit topologies. However, as shown in Fig. 1, most parts are connected by using a $50\ \Omega$ port in the conventional rectenna configuration. Therefore, the design of the impedance matching network is usually the most challenging part, particularly in multiband or broadband rectennas. Thus in previous work [19] [24], the structures of the impedance matching networks were complex for broadband and multiband rectennas, while the number of circuit components used in the matching network were very large (i.e., over 25 elements) to reduce the non-linear effects and produce a consistent performance. Consequently, the complex matching networks may introduce errors from manufacture, increase the cost and loss, and create additional problems.

In this work, we propose a novel method for broadband or multiband rectenna designs. The aim is to eliminate the need for impedance matching networks and to improve the overall performance of the rectenna. As shown in Fig. 2, the proposed new configuration only consists of three parts, wherein the antenna is changed to a special high impedance antenna which is very different from conventional ones. The impedance of the antenna is around 200 to $300\ \Omega$ for the real part and 0 to $300\ \Omega$ for the imaginary part in desired frequency band. The value of the antenna impedance ($X - jY$) may directly conjugate match with the input impedance of a specific rectifier ($X + jY$) within the desired frequency range but mismatch at other frequencies (to produce a filtering response), as depicted in Fig. 2. Thus a matching network can be eliminated and the proposed rectenna

can offer high conversion efficiency over a broad bandwidth. Moreover, since both the rectifier and the antenna are of relatively high input impedance, the effects on the reflection coefficient (S_{11}) of the rectenna caused by the impedance variation of the nonlinear elements (rectifying diodes) may not be very significant. Therefore, compared with the conventional 50 Ω (low impedance) matching system, the non-linear effects of the rectenna can be significantly reduced by using this new configuration. The rectenna may have a good performance in a range of operating conditions such as different input power levels, different load values, or even different types of rectifying diodes. In addition, the proposed rectenna configuration can reduce the total cost and avoid fabrication errors due to its very simple structure.

III. HIGH IMPEDANCE ANTENNA DESIGN

A. Off-Center-Fed Dipole Theory

There have been various types of high impedance antenna reported in literature [28] [29], but none of them can provide a constantly high impedance over a wide frequency range which is very important for realizing the proposed broadband high efficiency rectenna. There are no available approaches that can tune the antenna impedance over a wide frequency band to the desired values. Consequently, if these high impedance antennas were used without matching networks, the bandwidth of the rectenna could become very narrow.

Here, we propose a broadband high impedance antenna, the off-center-fed dipole (OCFD) antenna.

As depicted in Fig. 3, the OCFD antenna is different from a conventional center-fed symmetrical dipole antenna, where the two dipole arms are asymmetrical and have unequal lengths. The typical application of the OCFD is to realize a multiband antenna, since the resonant center-fed dipole has its fundamental frequency at f_0 and harmonics at $3f_0$, $5f_0$, $7f_0$, and so on. While the OCFD can resonate at f_0 , $2f_0$, $4f_0$, and $8f_0$ by offsetting the feed by $\lambda/4$ from the center [30]. Such OCFDs are very popular in the amateur radio community. Recently, some researchers use the OCFD to create a 90-degree phase delay and generate circular polarization radiation field for the antenna [31]. But, one of the major problems of the OCFD is that the radiation resistance of the antenna could be very high, thus it is required to use a 4:1 or 6:1 balun transformer to convert the impedance to the feeding port 50 ohms resistance [32]. This is a disadvantage for most of those applications using OCFDs (in a conventional 50 Ω feed system), but we may take advantage of this feature in the proposed rectenna design. The OCFD antenna may be well matched to a rectifier without using matching networks since the rectifiers are normally of high input impedance as well. If we assume a half wavelength center-fed dipole and an OCFD having the same total length and radiating the same power, as shown in Fig. 3. The currents at the feed points for the symmetrical and asymmetrical dipoles are I_S and I_{AS} respectively. From [38], the relationship between the currents can be expressed as

$$I_{AS} = I_S \sin \alpha \quad (1)$$

where α is the measured angle from one end in electrical

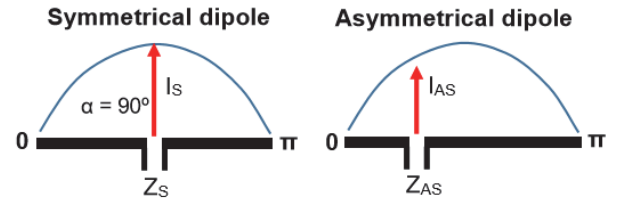


Fig. 3. The half wavelength center-fed symmetrical dipole and the off-center-fed asymmetrical dipole.

TABLE I

SIMULATED INPUT IMPEDANCE OF THE OFF-CENTER-FED DIPOLE

| Long arm (mm) | Short arm (mm) | Real part at f_0 (Ω) | Imaginary part at f_0 (Ω) |
|---------------|----------------|---------------------------------|--------------------------------------|
| 90 | 10 | 320 | -213 |
| 80 | 20 | 165 | -30 |
| 70 | 30 | 102 | -0.8 |
| 60 | 40 | 79 | 5.6 |
| 50 | 50 | 73 | 6.4 |

degrees (between 0 and π as shown in Fig. 3). Thus, the power radiated by both antennas can be calculated as

$$P_S = I_S^2 R_S \quad (2)$$

$$P_{AS} = I_{AS}^2 R_{AS} \quad (3)$$

where R_S and R_{AS} are the radiation resistances of the center-fed dipole and the OCFD respectively. Since we have assumed $P_S = P_{AS}$, thus we can obtain

$$\frac{R_S}{R_{AS}} = \frac{I_{AS}^2}{I_S^2} \quad (4)$$

Using (1), the relationship between the radiation resistances R_S and R_{AS} can be written as

$$R_S = \frac{R_{AS}}{(\sin \alpha)^2} \quad (5)$$

Thus, when $\alpha = 90^\circ$ or $(\pi/2)$, the dipole is center-fed since $\sin \alpha = 1$ and $R_S = R_{AS}$. It is demonstrated that the value of R_{AS} is always larger than the value of R_S if the dipole is off-center-fed. In addition, we could tune the radiation resistance of the OCFD to a desired value by changing the value of $\sin \alpha$ (position of the feed point).

In order to gain a better understanding, we study a simple OCFD antenna in free space with the aid of the CST software. Assume that the arms of the dipole are made by perfect electric conductor (PEC) wires with a diameter of 1 mm. The total length of the OCFD is 100 mm while the feeding port separation is 1 mm. If the antenna is considered as a typical half wavelength dipole, then the fundamental frequency should be about 1.5 GHz. The computed real part and imaginary part of the input impedance of the OCFD at 1.5 GHz are given in Table I for different feed locations. As can be seen from the table, the radiation resistance of the dipole is 73 Ω when the two arms have the same length. By changing the feed position, the radiation resistance can be increased where the value is about 320 Ω for the long arm being 90 mm and the short arm being 10 mm. Compared with the impedance of a symmetrical dipole (73 Ω), the OCFD has increased the impedance value up to 4.4 times. The imaginary part of the input impedance is around 0 ~ 6 Ω and the ratio of the long arm over the short arm is less than

7/3. Therefore, if the symmetrical dipole is of a broad bandwidth, the OCFD may produce constantly high impedance over the bandwidth of interest.

B. Broadband OCFD Antenna Design

A broadband center-fed symmetrical dipole is proposed as the starting point to design a broadband OCFD antenna. As shown in Fig. 4(a), the arms of the dipole are shaped as radial (bowtie) stubs to broaden the frequency bandwidth. The bowtie dipole antenna is a planar version of a biconical antenna. From [36], the characteristic impedance (Z_k) of an infinite biconical antenna is given by

$$Z_k = 120 \ln \cot(\theta/4) \quad (6)$$

where θ is the cone angle. Then the input impedance (Z_i) of the biconical antenna with a finite length can be written as

$$Z_i = Z_k \frac{Z_k + jZ_m \tan \beta l}{Z_m + jZ_k \tan \beta l} \quad (7)$$

where $\beta = 2\pi/\lambda$ (λ is the wavelength), l = cone length, and $Z_m = R_m + jX_m$. While the values of R_m and X_m are given by Schellkunoff [37] for a thin biconical antenna ($\theta < 5^\circ$). As indicated in [36], the VSWR of the biconical antenna can be less than 2 over a 2:1 bandwidth. Meanwhile, the input impedance of the bowtie dipole is similar to that of the biconical antenna, where the value of the impedance is a function of frequency, length of the arm (R) and cone angle (θ).

TABLE II
SIMULATED FREQUENCY BANDWIDTH OF THE BOWTIE DIPOLE

| | R = 40 mm | R = 50 mm | R = 60 mm |
|---------------------|---------------|---------------|---------------|
| $\theta = 10^\circ$ | 1.93–2.14 GHz | 1.83–1.93 GHz | 1.58–1.97 GHz |
| $\theta = 30^\circ$ | 1.93–2.28 GHz | 1.75–2.17 GHz | 1.58–1.98 GHz |
| $\theta = 50^\circ$ | 1.91–2.25 GHz | 1.73–2.19 GHz | 1.55–2 GHz |
| $\theta = 70^\circ$ | 1.91–2.28 GHz | 1.73–2.21 GHz | 1.55–2.03 GHz |

The aforementioned theories could be utilized to predict the initial performance (such as the frequency bandwidth) of this broadband antenna with a given dimension. But the actual performance might be varied in the simulation and measurement due to the practical configuration of the antenna (e.g., effects of PCB and feed). Therefore, in order to maintain the antenna performance, the major design parameters of the antenna should be further tuned using the software. As a design guide, the parametric effects (values of the R and θ) on the frequency bandwidth of the bowtie dipole (as shown in Fig. 4(a)) are studied. If the antenna is printed on a Rogers RT6002 board with a relative permittivity of 2.94 and a thickness of 1.52 mm, and it is fed by a pair of coplanar striplines (CPS) where the length (L) of each strip is 32 mm and the width (W) is 1.5 mm. The gap between the CPS is 1 mm. The antenna is modelled using the CST software. The simulated frequency bandwidth (for VSWR < 2 with 50 Ω port) of the bowtie dipole is shown in Table II for different cone angles and lengths of the arm.

From the results in Table II, it can be seen that the bowtie symmetrical dipole is indeed of a broad bandwidth. Moreover,

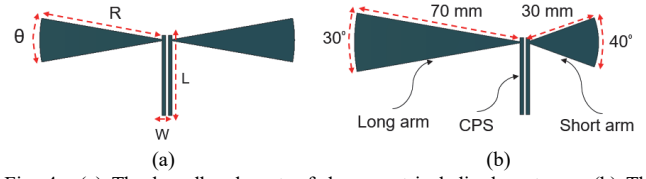


Fig. 4. (a) The broadband center-fed symmetrical dipole antenna. (b) The broadband off-center-fed dipole antenna.

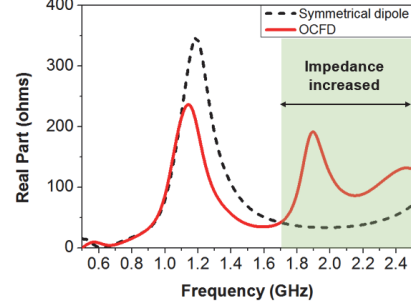


Fig. 5. The simulated real part of the impedance of the symmetrical dipole and the OCFD.

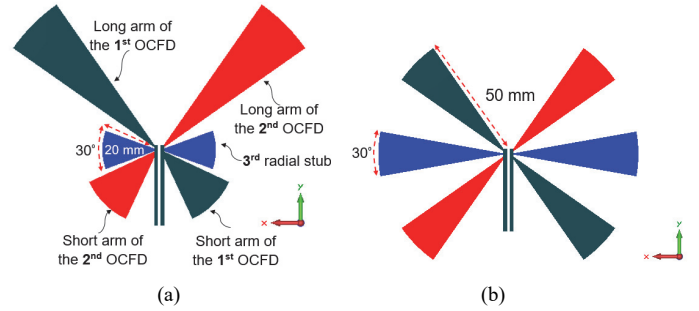


Fig. 6. (a) The proposed crossed off-center-fed dipole antenna. (b) The reference antenna with symmetrical arms for performance comparison.

the antenna could have a larger frequency bandwidth for larger cone angles, and have a lower resonant frequency band for larger dimensions (length of the arm). In this work, we select $R = 50$ mm, and $\theta = 30^\circ$ as an example, since the frequency band (from 1.75 to 2.17 GHz) has covered some popular mobile frequency bands such as the GSM1800 and UMTS2100. Hence, the arms of the symmetrical bowtie dipole have a radius of 50 mm and an angle of 30° for the radial stub structure. The maximum total length of the complete dipole antenna is about 100 mm.

To design the OCFD, the length of the longer arm is increased to 70 mm while the length of the shorter arm is therefore reduced to 30 mm. In addition, in order to enhance asymmetry between the arms, the circumference angle of the shorter arm is increased to 40° . The total length of the dipole is still of around 100 mm, as shown in Fig. 4(b). But the ratio of the long arm and the short arm has been changed from 5/5 to 7/3. In this scenario, the real part of the impedance over the frequency band may be increased while the imaginary part could be maintained over the resonant frequency band (as discussed in Table I). Fig. 5 shows the simulated real part of the input impedance of the symmetrical dipole and the OCFD. It can be seen that the impedance of the symmetrical dipole is around 50 Ω for frequencies between 1.75 and 2.4 GHz (around the 2nd and 3rd resonant frequency bands), which verifies the

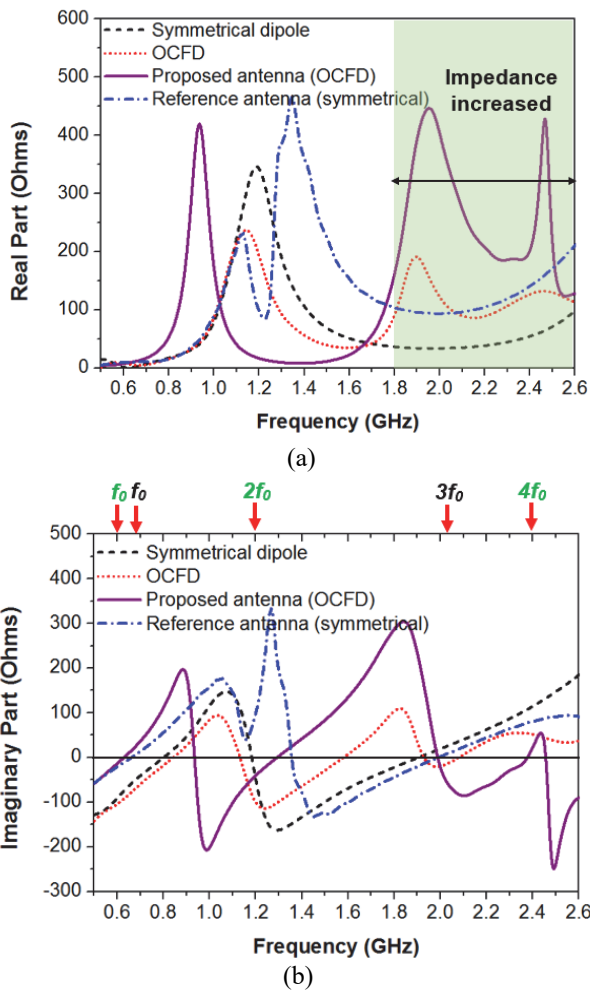


Fig. 7. The simulated input impedance of four different antennas; (a) Real part. (b) Imaginary part.

broadband performance of the antenna as depicted in Table II. However, the impedance of the OCFD is from 100 to 200 Ω over the frequency band between 1.8 and 2.5 GHz, which is much higher than that of the symmetrical dipole. It is shown that, by modifying a broadband symmetrical bowtie dipole to an OCFD, the antenna impedance is significantly increased over the desired resonant frequency range. In addition, the impedance for both antennas at the frequencies from 1.1 to 1.2 GHz is also very high (i.e. over 200 Ω), this is due to the anti-resonance of the dipole antenna [31].

The next step is to modify the proposed OCFD to a crossed OCFD by introducing another OCFD. As shown in Fig. 6 (a), the second OCFD (red) having the same dimensions as the first one, but they are orthogonal to each other. The purpose is to achieve dual polarization receiving capability and generate a vertically symmetrical radiation pattern for the antenna. Finally, another pair of radial stubs (blue) is inserted between the two OCFDs to further manipulate the impedance. The final antenna layout is shown in Fig. 6(a) which looks symmetrical from left to right as a whole. For comparison, a reference antenna consisting of three dipoles with symmetrical arms is studied. As shown in Fig. 6(b), the arms of the reference

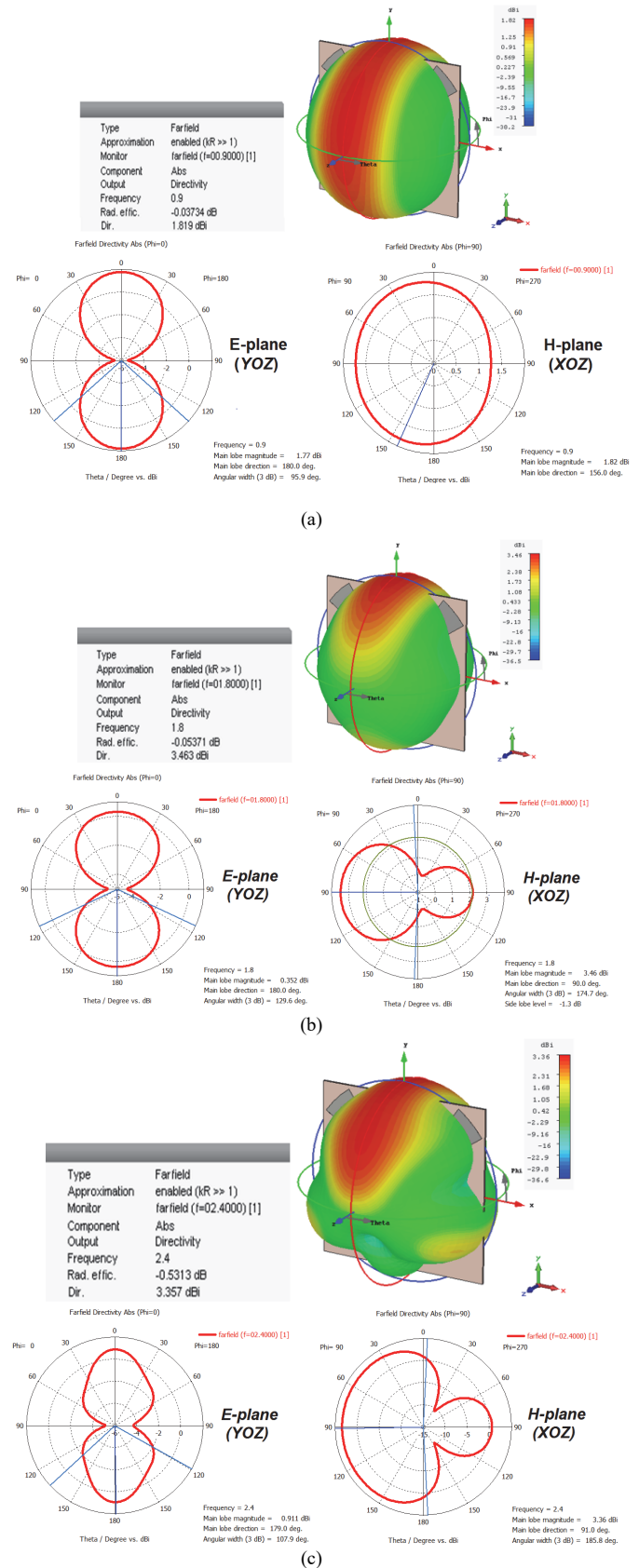


Fig. 8. The simulated 3D patterns with directivities and 2D patterns over E-plane and H-plane of the proposed antenna at (a) 0.9 GHz, (b) 1.8 GHz, and (c) 2.4 GHz.

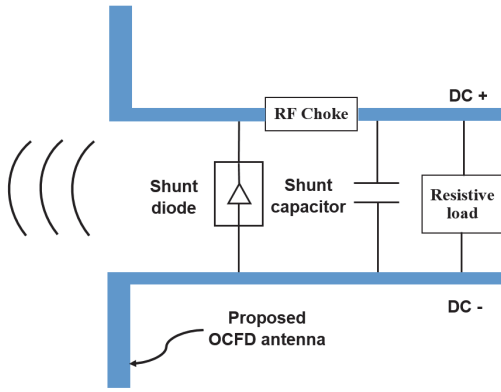


Fig. 9. Configuration of a single shunt diode (Class F) rectifier with a dipole antenna.

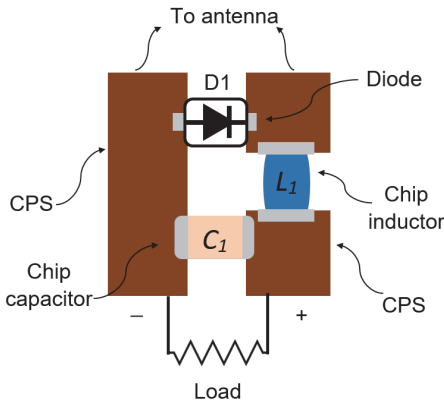


Fig. 10. Configuration of the proposed rectifier on coplanar striplines (CPS).

TABLE III
CIRCUIT COMPONENTS USED IN THE DESIGN

| Component name | Nominal Value | Part number and supplier |
|----------------|-----------------------|----------------------------|
| D1 | Schottky diode | SMS7630-079LF, Skyworks |
| L1 | 47 nH chip inductor | 0603HP47N, Coilcraft |
| C1 | 100 nF chip capacitor | GRM188R71H104JA93D, Murata |

antenna have a radius of 50 mm and a circumference angle of 30° for the radial stub. Thus the reference antenna and the proposed antenna have the same electrical length (100 mm). The simulated real part and imaginary part of the input impedance of four different antennas (single symmetrical dipole, single OCFD, proposed OCFD, and reference antenna) are shown in Figs. 7(a) and (b). It can be seen that the real part of the input impedance of the proposed broadband OCFD antenna is above 180Ω (up to 450Ω) for the frequency band between 1.8 GHz and 2.5 GHz, which is much higher than that of the reference antenna (around 100Ω). In addition, the proposed antenna has shifted the high-impedance (about 400Ω) frequency from around 1.4 GHz to around 0.9 GHz. This is likely due to the coupling effects among the three dipoles. The imaginary part of the reference antenna is around 0Ω at frequencies around 0.7 GHz and 2.1 GHz, which are f_0 and $3f_0$

respectively. While the imaginary part of the proposed OCFD is around 0Ω at resonant frequencies 0.6 GHz, 1.2 GHz and 2.4 GHz, which are f_0 , $2f_0$, and $4f_0$ respectively. These results have demonstrated that the simulated results agree with the OCFD theory as discussed in Section III-A. Furthermore, the imaginary part of the impedance of the antenna over the resonant frequency band from 1.4 to 2 GHz turns from negative values (for the reference antenna) to positive values (for the proposed antenna). As shown in Fig. 7(b), the value of the imaginary part of the proposed antenna impedance varies between 0 and 300Ω over the desired frequency band. This feature could help the proposed antenna to produce a better conjugate matching with the rectifier, since the imaginary part of the impedance of the rectifier normally varies between -700 and 0Ω as we discussed earlier. The simulated 3D radiation patterns of the proposed antenna at the frequencies of interest are depicted in Fig. 8. The 2D polar plots of antenna patterns in *E-plane* and *H-plane* are shown as well. Here we have only showed the directivity (maximum gain) of the antenna (without taking the mismatch loss into account). From Fig. 8, it can be seen that the antenna has symmetrical patterns about YOZ plane with a maximum directivity of 1.8 dBi at 0.9 GHz, 3.5 dBi at 1.8 GHz and 3.3 dBi at 2.4 GHz. The antenna is more directive towards the long arm direction at 1.8 GHz and 2.4 GHz with the half-power beam-widths (HPBW) of around 174° and 185° respectively. The HPBW is about 96° at 0.9 GHz.

Therefore, the proposed broadband OCFD antenna has obtained high impedance over a wide frequency range. The proposed design is just an example to illustrate the proposed new method. The details of the dipole could be modified according to the frequency of interest.

IV. RECTENNA INTEGRATION

A. Rectifier Configuration

The proposed high impedance OCFD antenna may directly conjugate match with the input impedance of a rectifier over a wide frequency band. The rectifier should only consist of few circuit components for rectification, DC storage and output. A single shunt diode rectifier is selected due to its very simple structure and high conversion efficiency [33]. The configuration of the single shunt diode rectifier with a dipole antenna is depicted in Fig. 9. The shunt diode is used as the rectifying element and the diodes for high frequency (e.g., $f > 1$ GHz) applications are normally Schottky diodes such as SMS7630 (from Skyworks) and HSMS2860 (from Avago). A shunt capacitor after the diode is used to store DC power and smooth the DC output waveforms. In addition, a series connected RF choke is placed between the diode and capacitor to block AC components generated from the diode. In this design, a typical inductor of 47 nH is selected as the RF choke. To have a better configuration on the PCB, the proposed antenna and rectifier are both fed by CPS (or twin-wire conducting strips). The topology of the rectifier configured with the conducting strips extended from the OCFD antenna is shown in Fig. 10. The values and part numbers of the circuit components are given in Table III.

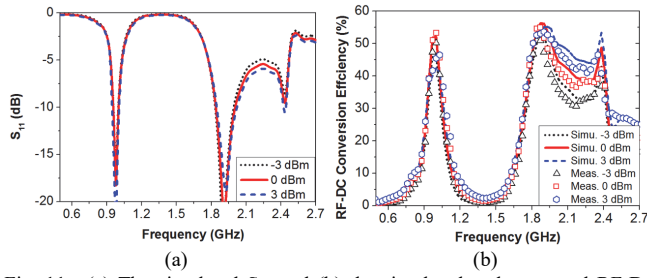


Fig. 11. (a) The simulated S_{11} and (b) the simulated and measured RF-DC conversion efficiency of the rectenna at three different input power levels. The load resistance is 400 Ω .

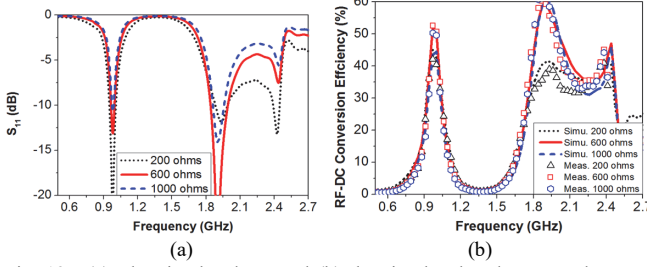


Fig. 12. (a) The simulated S_{11} and (b) the simulated and measured RF-DC conversion efficiency of the rectenna at three different load values. The input power level is 0 dBm.

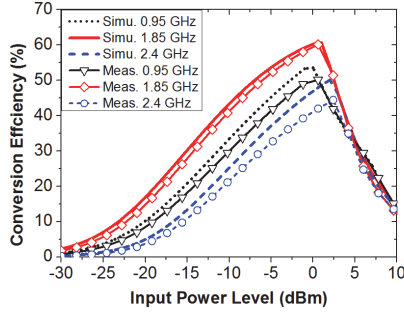


Fig. 13. The simulated and measured conversion efficiency of the rectenna versus input power level at three frequencies. The load resistance is 600 Ω .

The rectifier is built and simulated by using the ADS software. To improve the accuracy of results, the diode is modelled by using a non-linear SPICE model with parasitic elements provided by the suppliers (such as Skyworks). The chip inductor and capacitor are modelled by using the real product models, including the S-parameter files, provided by Murata and Coilcraft. Since the proposed design can eliminate the matching network between the antenna and the rectifier, thus the rectifying circuit is indeed simplified. The frequency domain power source port is used in the simulation, and the port impedance is defined as the impedance of the proposed OCFD antenna by using the touchstone S1P files exported from the CST, similarly to the results shown in Figs. 7(a) and (b).

B. Rectenna Performance

After the complete rectenna has been designed, its performance is evaluated by using the Harmonic Balance (HB) simulation and the Large Signal S-Parameter (LSSP) simulation using the ADS. The performances of the proposed rectenna in terms of the reflection coefficient (S_{11}) and RF-DC conversion efficiency are shown in Figs. 11 to 13. The RF-DC conversion efficiency is obtained by

$$\eta_{RF-DC} = \frac{P_{DC}}{P_{in}} \quad (8)$$

where P_{DC} is the output DC power and P_{in} is the input RF power to the antenna. The S_{11} (simulated) and conversion efficiency (simulated and measured) of the rectenna at different input power levels are shown in Figs. 11(a) and (b) as a function of frequency. A typical load resistor of 400 Ω is selected. From Fig. 11, it can be seen that the rectenna covers the desired broad frequency band from 1.8 to 2.5 GHz and an additional frequency band around 1 GHz. The S_{11} of the rectenna is lower than -10 dB between 1.8 and 2 GHz and around 1 GHz. The conversion efficiency is higher than 40% (up to 55%) over the entire frequency band of interest for the input power level of 0 dBm (1 mW). In addition, when the input power is doubled (3 dBm) or halved (-3 dBm), the reflection coefficients are always smaller than -6 dB from 1.8 to 2.5 GHz, while the efficiency over the band of interest is still high (e.g., greater than 35%).

Figs. 12 (a) and (b) depict the S_{11} (simulated) and conversion efficiency (simulated and measured) of the rectenna for different load values. It can be seen that the efficiency is higher than 30% (up to 60%) for the load values from 200 to 1000 Ω and for the frequencies between 1.8 and 2.5 and the around 1 GHz. It is demonstrated that the nonlinear effects linked to the input power and load are reduced in the proposed broadband rectenna, which verifies our predictions in Section II. The simulated and measured conversion efficiency of the rectenna versus input power level is shown in Fig. 13 at three frequencies. It can be seen that the rectenna has the highest efficiency at the input power of around 0 dBm. This is because the selected diode (SMS7630) has reached its reverse breakdown voltage. Since this diode has a very low forward bias voltage (150 mV) and a low breakdown voltage (2 V) [34], it is normally applied in low input power (e.g., from -30 to 0 dBm) applications. For high input power applications (e.g., > 10 dBm) and higher conversion efficiency (e.g., up to 80%), other diodes with a higher breakdown voltage could be selected.

V. RECTENNA MEASUREMENTS AND VALIDATIONS

The fabricated prototype rectenna is shown in Fig. 14 and the measurement setup is depicted in Fig. 15. Since the proposed antenna has been integrated with the rectifier, the S_{11} of the rectenna cannot be measured directly. A standard horn antenna R&S®HF906 was used to transmit the RF power. A 30 dB gain power amplifier (PA) amplifies the signal generated by an RF signal generator (Keithley2920). The rectenna was configured to receive the signal at a distance of 1 meter (in antenna far field). The output DC voltage (V_{DC}) was measured by using a voltage meter and the output DC power can be obtained by using $P_{out} = V_{DC}^2/R$, where R is the load resistance.

The available power to the transmitting horn antenna was measured by using a power meter, thus the received RF power by the rectenna can be estimated by using the Friis transmission equation [35].

$$P_r = P_t + G_t + G_r + 20 \log_{10} \frac{\lambda}{4\pi r} \quad (9)$$

where P_r is the received power in dBm, P_t is the power obtained from the power meter in dBm, G_t is the realized gain of the transmitting antenna in dB, G_r is the realized gain of the

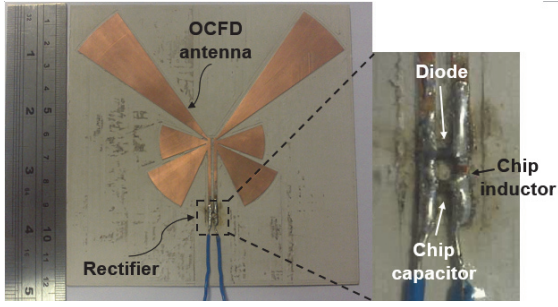


Fig. 14. The fabricated prototype rectenna. The enlarge view of the rectifier is shown as well.

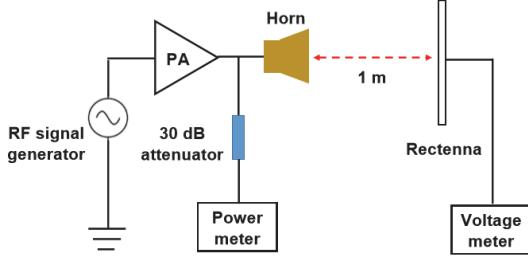


Fig. 15. The measurement setup of the rectenna.

receiving antenna (rectenna) in dB, λ is the wavelength, and r is the distance between the TX and RX antennas ($r = 1$ m).

As discussed earlier, the proposed rectenna can reduce the effects of the nonlinearity of the rectifier and match well to a wide range of load impedance values. Thus the rectenna may perform well even when different types of diodes are used. This advantage is normally not available in the conventional rectenna designs, since the input impedance and characteristics of the diodes can be very different. Thus, in order to validate this point, the proposed rectenna was measured by using different types of Schottky diodes such as HSMS2850, HSMS2860, and HSMS2820. The measured conversion efficiency versus input power level is shown in Fig. 16 along with simulated results. High conversion efficiency is obtained in all cases. When the load is selected as 500Ω and the frequency is selected as 1.85 GHz, we have $G_t = 8.5$ dBi, $G_r = 3.45$ dBi, $\lambda = 0.162$ m, and $r = 1$ m. Using (9), the correlation between the transmitting power and the receiving power can be obtained as:

$$Pr \text{ (dBm)} = Pt \text{ (dBm)} - 25.84 \text{ dB.} \quad (10)$$

It can be seen that the maximum conversion efficiency and the corresponding input powers of the rectenna are 60% at 0 dBm, 65% at 5 dBm, 70% at 10 dBm, and 75% at 20 dBm for using the Schottky diodes SMS7630, HSMS2850, HSMS2860, and HSMS2820 respectively. The peak efficiency is realized at different input power levels. This is because the breakdown voltages for the selected diodes are different, which are 2 V (SMS7630), 3.8 V (HSMS2850), 7 V (HSMS2860), and 15 V (HSMS2820) respectively. The efficiency is much higher at high input power levels for using the diodes with large breakdown voltages (e.g. HSMS2820), while the efficiency is higher at low input power levels for using the diodes with small forward bias voltages (e.g. SMS7630). The simulated and measured conversion efficiency of the rectenna (using the four

TABLE IV
RECTENNA PERFORMANCE FOR USING DIFFERENT DIODES

| Schottky diodes name | Simulated input impedance under the same condition (Ω) | Optimal input power level | Maximum conversion efficiency | Optimal load resistance range (Ω) |
|----------------------|---|---------------------------|-------------------------------|--|
| SMS7630 | $173 - j 36$ | 0 dBm | 60% | 250–1500 |
| HSMS2850 | $325 - j 57$ | 5 dBm | 65% | 200–2000 |
| HSMS2860 | $349 - j 166$ | 10 dBm | 70% | 200–2500 |
| HSMS2820 | $82 - j 145$ | 20 dBm | 75% | 250–3000 |

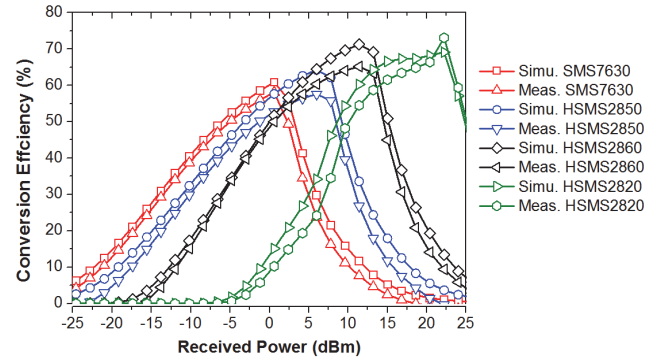


Fig. 16. The simulated and measured conversion efficiency of the rectenna versus input power level for using different types of Schottky diodes. The frequency is 1.85 GHz.

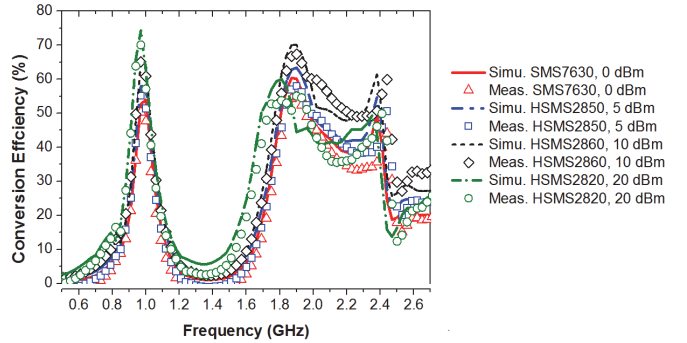


Fig. 17. The simulated and measured conversion efficiency of the rectenna versus frequency for using different types of Schottky diodes at the optimal input power levels. The load resistance is 500Ω .

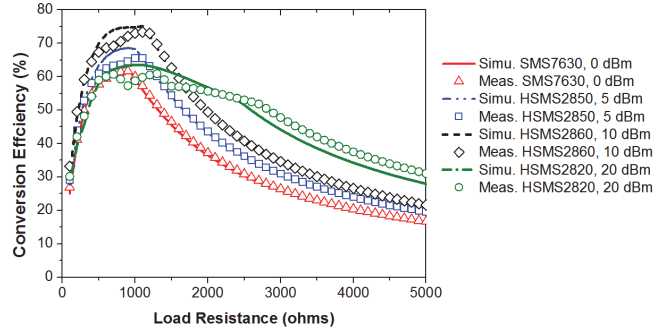


Fig. 18. The simulated and measured conversion efficiency of the rectenna versus load resistance for using different types of Schottky diodes at the optimal input power levels. The frequency is 1.85 GHz.

different diodes) are depicted in Fig. 17 as a function of the frequency. The load is still 500Ω while the input power levels

TABLE V
COMPARISON OF THE PROPOSED RECTENNA AND RELATED DESIGNS

| Ref. (year) | Frequency (GHz) | Use of impedance matching networks | Complexity of the overall design | Maximum conversion efficiency (%) | Input power level for conversion efficiency > 60% | Optimal load range with good performance (k Ω) | Type of Schottky diode |
|-------------------------|---|------------------------------------|----------------------------------|-----------------------------------|---|--|------------------------|
| [18] (2015) | Four-band 0.9, 1.8, 2.1, 2.4 | Yes | Very complex | 65 at 0 dBm | -5 to 0 dBm | 11 | MSS20-141 |
| [19] (2015) | Broad-band 1.8 – 2.5 | Yes | Complex | 70 at 0 dBm | -7 to 0 dBm | 14.7 | SMS7630 |
| [20] (2015) | Dual-band 0.915, 2.45 | Yes | Complex | 70 at 0 dBm | -5 to 0 dBm | 0.5 – 3 | SMS7630 |
| [23] (2012) | Tunable 0.9 – 2.45 | Yes | Very complex | 80 at 30 dBm | Tunable 5 to 30 dBm | 1 – 4 | Tunable |
| [24] (2016) | Six-band 0.55, 0.75, 0.9, 1.85, 2.15, 2.45 | Yes | Very complex | 68 at -5 dBm | -5 to 0 dBm | 10 – 75 | SMS7630 |
| [25] (2012) | Single-band 2.45 | No | Simple | 70 at -5 dBm | -10 to 5 dBm | 2.8 | HSMS2852 |
| [26] (2004) | Broad-band 2 – 18 | No | Medium | 20 at 17 dBm | Not available | 0.6 | SMS7630 |
| This work (2016) | Broad-band 0.9 – 1.1, 1.8 – 2.5 | No | Simplest | 75 at 20 dBm | Tunable 0 to 23 dBm | 0.2 – 2 | Tunable |

are selected as the optimal input powers for these diodes (e.g. 0 dBm for SMS7630, 5 dBm for HSMS2850, 10 dBm for HSMS2860, and 20 dBm for HSMS2820). Note that in the measurements, the correlation between the transmitting power and the receiving power (as given in (9)) might be changed if the frequencies are different. Thus the transmitting power should be tuned to make sure that the received power is approximately a constant value in the broadband (e.g. 0 dBm for the frequencies from 0.9 to 3 GHz).

From the results in Fig. 17, it can be seen that the rectenna is still of broadband performance (1.8 to 2.5 GHz) when using different diodes, and the conversion efficiency is constantly high over the frequency bandwidth of interest for the selected input power levels. Figs. 16 and 17 have shown a good agreement between the simulated and measured results.

Fig. 18 shows the simulated and measured conversion efficiency by using different load resistances. The frequency is selected as 1.85 GHz while the input power levels are still set as the optimal input powers. In reality, the load impedance may vary over a large range in different applications, thus it is important to reduce the sensitivity of efficiency vs. load variation in a nonlinear system (rectenna). From Fig. 18 it can be seen that, when using different diodes, the efficiency of the rectenna is constantly high (from 40% to 75%) for the load values between 200 Ω and 2000 Ω , then the efficiency starts to decrease due to the impedance mismatch between the antenna and the rectifier. It demonstrates that the nonlinear effects have been reduced over the load range from 200 to 2000 Ω . For other load values, the details of the rectenna can be modified to achieve good performance.

According to the results in Figs. 16–18, the performance of the rectenna by using different diodes is summarized in Table IV. The simulated input impedance of the rectifier is shown under the same condition (frequency: 1.85 GHz, input power: 10 dBm, and load: 500 Ω). The impedance is very different for different types of diodes, but our rectenna can still be well configured with these diodes without using matching networks. It is demonstrated that the proposed broadband rectenna can

work well in different operating conditions. The non-linear effects have been reduced. The matching networks have indeed been eliminated. In addition, the optimal input power level of the device is tunable (from 0 to 23 dBm) by selecting appropriate diodes so that the conversion efficiency of the broadband rectenna can be always higher than 60% (as shown in Fig. 16). This is very important for WPT or WEH used in practice.

A comparison between our rectenna and other related work is shown in Table V. It can be seen that our design seems to be the only one without using the matching networks, but still achieves high conversion efficiency over a relatively wide frequency band. The conversion efficiency of our design is comparable with that of the other work used matching networks, while the performance of the rectenna is reasonably well in a range of input powers and load impedance. In addition, our device is also the only one which can use different types of diodes without changing any other part of the circuit. The structure of our design is the simplest for broadband rectennas with similar performance. The proposed rectenna is of good industrial value due to its simplicity and universality, and is of good practical value due to its consistent performance in different operating conditions.

Also, the proposed concept for eliminating the matching networks is not just limited in the presented design, and can also be used in other similar non-linear systems.

VI. CONCLUSION

A novel method for eliminating the matching network of broadband rectennas has been presented. An OCFD antenna has been designed, where the antenna impedance can be tuned to directly match with the rectifier. The proposed rectenna is of a broad bandwidth and high efficiency, and has excellent performance in different operating conditions. The measured performance has shown that the operating frequencies of the experimental rectenna are from 0.9 to 1.1 GHz and from 1.8 to 2.5 GHz (which are the typical cellular mobile, WLAN and ISM bands), while the maximum conversion efficiency is up to

75% and the optimal input power range is tunable from 0 dBm to 23 dBm by selecting appropriate diodes. In addition, the rectenna has a very simple structure and low cost. Considering the excellent overall performance of the proposed rectenna, it is suitable for high efficiency WPT and WEH applications. The design concept is easy to follow while its details can be optimized for different applications.

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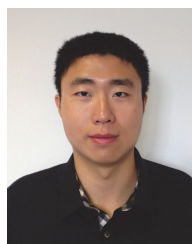
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